

LA78040B

The vertical deflection Output IC for TVs and CRT display

The LA78040B is a vertical deflection output IC for TVs and CRT displays with excellent image quality that use a BUS control system signal processing IC. This IC can drive the direct (even including a DC component) deflection yoke with the sawtooth wave output from the BUS control system signal processing IC.

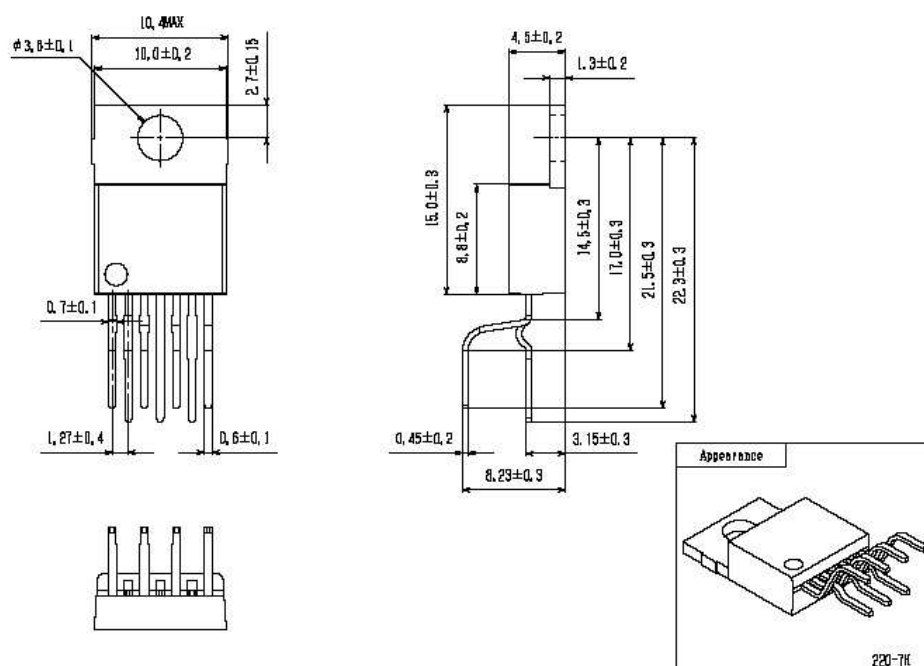
LA78040B 1.8Ap-p Small and Medium screen sets

Features

- Low power dissipation due to built-in pump-up circuit .
- Vertical output circuit .
- Thermal protection circuit built in .
- Excellent crossover characteristics .
- DC coupling possible

Package

TO220-7H



*: These specifications are subject to change without notice.

LA78040B

c. Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	Vcc2max		34	V
Output block supply voltage	Vcc6max		70	V
Allowable power dissipation	Pdmax	With arbitrarily large heat sink	9	W
Deflection output current	I5max		-1.4 to +1.4	Ap-o
Thermal resistance	θ_{j-c}		3	°C/W
Operating temperature	Topg		-20 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

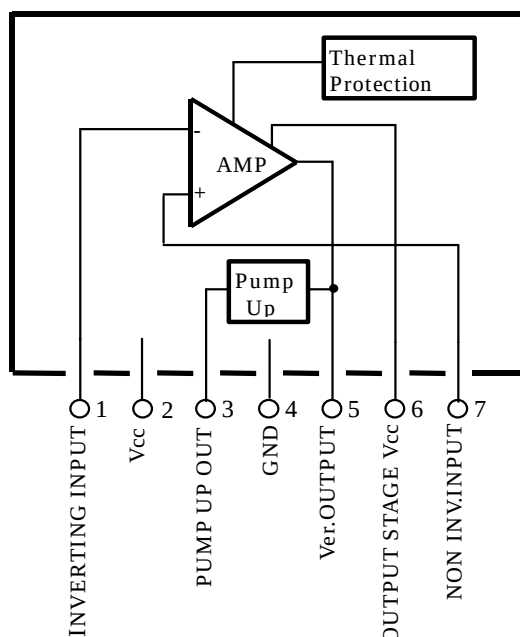
d. Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	Vcc2max		24	V
Operating supply voltage range	Vcc2op		16 to 33	V
Recommended deflection output current	I5p-p		to 1.8	Ap-p

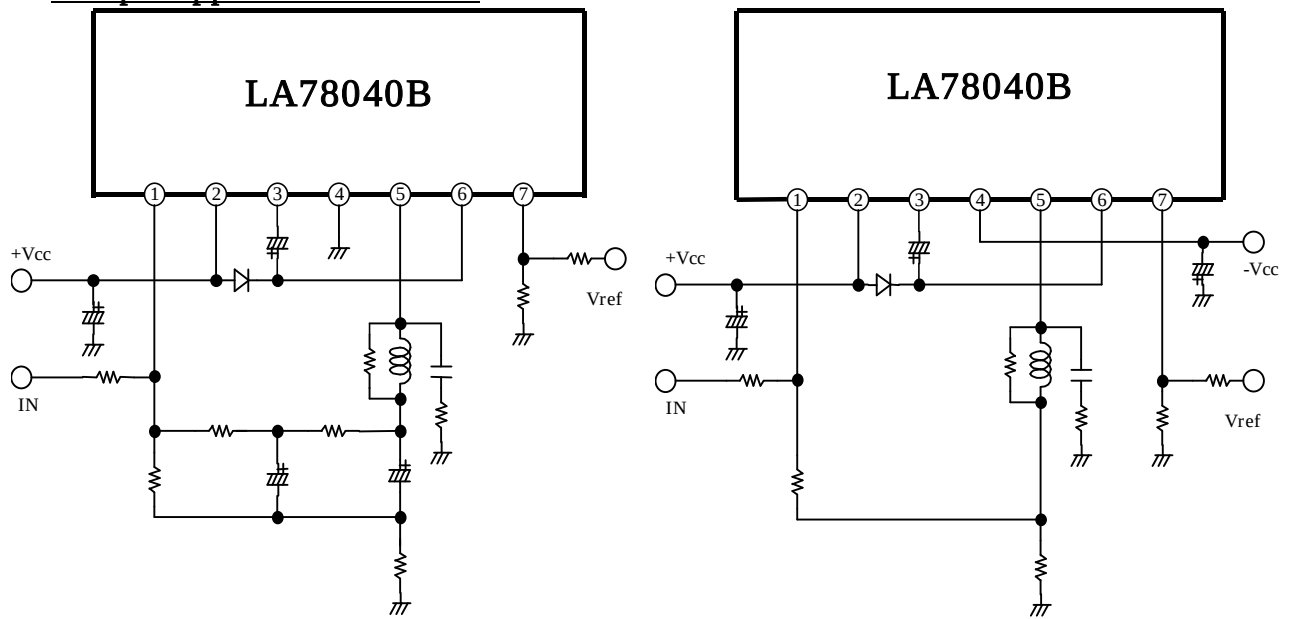
e. Operating Characteristics at Ta = 25°C, Vcc2=24V

Parameter	Symbol	Conditions	min	typ	max	Unit
Deflection output saturation voltage (lower)	Vsat5-4	I5=0.9A			1.3	V
Deflection output saturation voltage (upper)	Vsat6-5	I5=-0.9A			3.2	V
Pump-up charge saturation voltage	Vsat3-4	I3=20mA			1.8	V
Pump-up discharge saturation voltage	Vsat2-3	I3=-0.9A			3.0	V
Idling current	Idl		20		50	mA
Midpoint voltage	Vmid		11.0	12.0	13.0	V

Block Diagram



Sample application circuits



Sample application circuits2

